Dimitris Gizopoulos
Title: Associate Professor on Computer Architecture
Sector: Computer Systems and Applications
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Academic Qualifications:

- Ph.D. in Informatics (1997) Dept. of Informatics and Telecommunications, University of Athens
- B.Sc. in Computer Engineering (1992) Dept. of Computer Engineering and Informatics, University of Patras

Appointments:

- May 2011 – present: Associate Professor, Dept. of Informatics & Telecom., University of Athens
- November 1999 – May 2011: Associate Professor (previously Assistant Professor and Lecturer), Dept. of Informatics, University of Piraeus
- May 1999 - November 1999: Post-Doctoral Fellow, NCSR "Demokritos", Institute of Informatics & Telecom.
- January 1998 – May 1999: Digital Systems Development Engineer, Private Sector companies

Teaching Activities: (current)

Undergraduate courses
- Computer Architecture I
- Computer Architecture II

Postgraduate courses
- Modern Microprocessor Architectures

Supervisor of 2 completed and 3 ongoing Ph.D. dissertations.

Research Interests/Activities: (last 10 years)

- Dependable and Fault Tolerant Computer Architecture
- Microprocessors and Memories Reliability and Self-Testing
- Online Errors and Bug Detection and Recovery
- Embedded systems design and validation

Scientific Publications/Citations:

- 38 publications in refereed journals (23 IEEE transactions and magazines)
- 67 refereed conference publications (9 ITC, 8 DATE, 1 DAC)
- More than 700 citations. (H-index 18)
- Author of a book and editor of a second (both Springer)
- Invited tutorials, panels, talks.
- 1 US patent

Top five publications

- "An Effective Built-In Self-Test Scheme for Array Multipliers", D. Gizopoulos, A. Paschalis and Y. Zorian. IEEE Trans. on Computers, Sept. 1999

- "Software-Based Self-Testing of Symmetric Shared-Memory Multiprocessors", A.Apostolakis, D.Gizopoulos, M.Psarakis, A.Paschalis, IEEE Trans. on Computers, vol. 58, no. 12, pp. 1682-1694, December 2009
- "Systematic Software-Based Self-Test for Pipelined Processors", D.Gizopoulos, M.Psarakis, M.Hatzimihail, M.Maniatakos, A.Paschalis, A.Raghunathan, S.Ravi, IEEE Trans. on Very Large Scale Integration Systems, vol. 16, no. 11, pp. 1441-1453, November 2008.
- "Architectures for Online Error Detection and Recovery in Multicore Processors", D.Gizopoulos, M.Psarakis, S.V.Adve, P.Ramachandran S.K.S.Hari, D.Sorin, A.Meixner, A.Biswas, X.Vera, ACM/IEEE Design, Automation, and Test in Europe Conference (DATE 2011), Nice, France, April 2011.
- "MT-SBST: Self-Test Optimization in Multithreaded Multicore Architectures", N.Foutris, M.Psarakis, D.Gizopoulos, X.Vera, A.Gonzalez, IEEE International Test Conference (ITC 2010), Austin, Texas, USA, November 2010.

Distinctions:

- Associate Editor: IEEE Transactions on VLSI Systems (April 2009 present), IEEE Transactions on Computers (February 2007 – present), IEEE Design & Test of Computers (January 2002 – present).
- Guest Editor for four Special Issues in IEEE Transactions on Computers, IEEE Transactions on VLSI Systems, IEEE Design & Test of Computers, IEEE Communications Magazine.
- IEEE Outstanding Contribution Award (2004).
- IEEE Meritorious Service Award (2009, 2006, 2005).
- IEEE Computer Society Golden Core Member recognition (2005).

Other Professional Activities:

- General chair for 3 IEEE Conferences.
- Program chair for 10 IEEE Conferences.
- Steering Committee member for 3 IEEE Conferences.
- Organizing Committee member for more than 40 IEEE/ACM Conferences.
- Program Committee member for more than 80 IEEE/ACM Conferences.
- Executive Committee member of IEEE Computer Society's Test Technology Technical Council (also Tutorials and Education group chair)